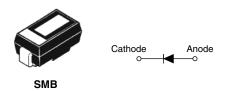
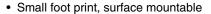


Vishay High Power Products

Schottky Rectifier, 1.0 A



FEATURES





- Low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The 10BQ040PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

PRODUCT SUMMARY		
I _{F(AV)}	1.0 A	
V_R	40 V	

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	1.0	А	
V_{RRM}		40	V	
I _{FSM}	t _p = 5 μs sine	430	Α	
V _F	1.0 Apk, T _J = 125 °C	0.49	V	
T _J	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	10BQ040PbF	UNITS	
Maximum DC reverse voltage	V_{R}	40	V	
Maximum working peak reverse voltage	V_{RWM}	40	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _L = 112 °C, rectangular waveform		1.0	Α
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	430	А
	I _{FSM}	10 ms sine or 6 ms rect. pulse		45	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	Α

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	1 A	T _J = 25 °C	0.53	V
		2 A		0.70	
See fig. 1	V FM (*)	1 A	T _J = 125 °C	0.49	
		2 A		0.64	
Maximum reverse leakage current	Maximum reverse leakage current See fig. 2	T _J = 25 °C	V _R = Rated V _R	0.1	- mA
See fig. 2		T _J = 125 °C		4	
Typical junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz) 25 °C		80	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nΗ
Maximum voltage rate of charge	dV/dt	Rated V _R 10 000 V		V/µs	

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/VV
Approximate weight			0.10	g
Approximate weight		0.003	OZ.	
Marking device		Case style SMB (similar DO-214AA)	V-	1F

Notes

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⁽¹⁾ $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB



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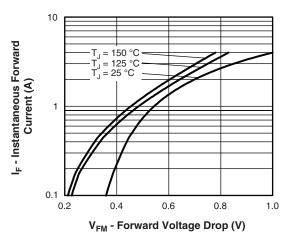


Fig. 1 - Maximum Forward Voltage Drop Characteristics

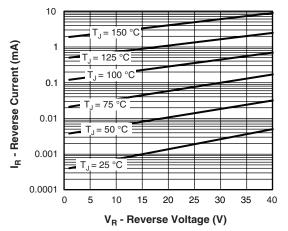


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

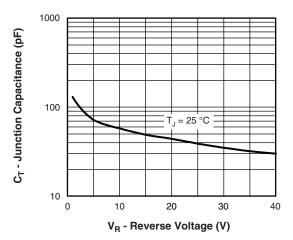


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

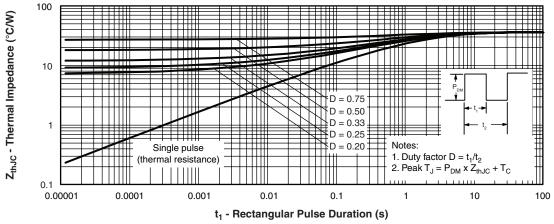


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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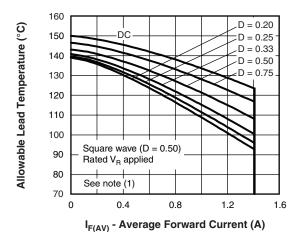
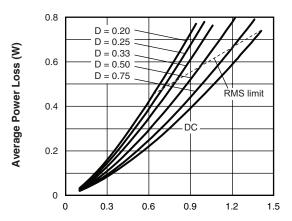


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 6 - Maximum Average Forward Dissipation vs.
Average Forward Current

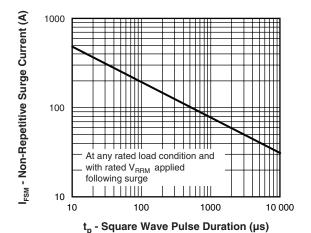


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

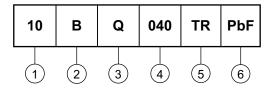
Note



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ORDERING INFORMATION TABLE

Device code



1 - Current rating

2 - B = Single lead diode

3 - Q = Schottky "Q" series

4 - Voltage rating (040 = 40 V)

• None = Box (1000 pieces)

• TR = Tape and reel (3000 pieces)

6 - None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS		
Dimensions	http://www.vishay.com/doc?95017	
Part marking information	http://www.vishay.com/doc?95029	
Packaging information	http://www.vishay.com/doc?95034	

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